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Sheet 1 of 3

Form PTO-1449 (REV. 8-88)		U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE		ATTY. DOCKET NO.		Serial No.(cont. of S/N 08/941,254)	
				500.32032CC6			
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				8/10/98		2818	

U.S. PATENT DOCUMENTS							
EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE	
04	A	4807114	2/89	Itoh	364	200	
04	B	5065364	11/91	Atwood, et al.	365	218	
04	C	4698750	10/87	Wilkie, et al.	365	218	
04	D	5070473	12/91	Takano, et al.	395	725	
04	E	5329492	7/94	Mochizuki	365	230.03	11/19/92
04	F	4796235	1/89	Sparks, et al.	365	228	
04	G	4402065	8/83	Taylor	365	189	
04	H	4785425	11/88	Lavelle	365	52	
04	I	5097445	3/92	Yamauchi	365	195	12/14/89
04	J	5185718	2/93	Rinerson, et al.	365	185	2/19/91
04	K	5283758	2/94	Nakayama, et al.	365	185	6/12/90

FOREIGN PATENT DOCUMENTS							
DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION		
					YES	NO	
04	L	2-289997	11/90	Japan		abstr. only	
04	M	59-29448	2/84	Japan		abstr. only	
04	N	3-078195	4/91	Japan		abstr. only	
04	O	3-187263	8/91	Japan		abstr. only	
04	P	3-014272	1/91	Japan		abstr. only	

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)		
04	Q	EDN Electrical Design News, Vol. 28, No. 21, Oct. 13, 1983, pp. 177-188.
04	R	Patent Abstracts of Japan, Vol. 14, No. 542 (p-1137), Sept. 4, 1990.
04	S	Electronic Engineering, Vol. 63, No. 775, July 1991, p. 49.

EXAMINER	DATE CONSIDERED
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Applicant

K. Matsubara, et al.

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04	T	5175840	12/92	Sawase, et al.	395	425	10/86
04	U	5369647	11/94	Kreifels, et al.	371	25.1	12/16/91
04	V	5297096	3/94	Terada, et al.	365	218	6/7/91
04	W	4953129	8/90	Kobayashi, et al.	365	203	
04	X	5377145	12/94	Kynett, et al.	365	189.05	2/11/91
04	Y	5222046	6/93	Kreifels, et al.	365	185	2/17/88
04	Z	5321845	6/94	Sawase, et al.	395	800	8/31/88
04	AA	5305276	4/94	Uenoyama	365	230.01	9/11/92
04	AB	4701886	10/87	Sakakibara	371	21.1	
04	AC	4783764	11/88	Tsuchiya, et al.	365	174	
04	AD	5109359	4/92	Sakakibara, et al.	365	189.07	2/27/90

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							YES	NO
04	AE	3-250495	11/91	Japan	—	—	abstr. only	
04	AF	2-241060	9/90	Japan	—	—	abstr. only	
04	AG	1-161469	6/89	Japan	—	—		
04	AH	0392895	10/90	European Pat. Appln.	—	—		
04	AI	0373672	6/90	European Pat. Appln.	—	—		

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04	AJ	IBM Technical Disclosure Bulletin, Vol. 32, No. 10B, pp. 141-142, 3/1990, "Method of Reducing DRAM Power Dissipation with Segmented Bitlines".
04	AK	Hitachi IC Memory Data Book 1, p. 872, Sept. 1981.

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94	AM	5088023	2/92	Nakamura, et al.	395	430	
94	AN	5255244	10/93	Dey	365	233	6/21/91
94	AO	5200600	4/93	Shinagawa	235	492	8/25/89
94	AP	4887234	12/89	Iijima	364	900	
94	AQ	5402383	3/95	Akaogi	365	218	6/21/93
94	AR	5581503	12/96	Matsubara, et al.	365	185.33	3/16/93
94	AS	5615159	3/97	Roohparvar	365	201	11/28/95
94	AT	5444664	8/95	Kuroda, et al.	365	226	7/13/94
94	AU	5566125	10/96	Fazio, et al.	365	45	1/21/94
94	AV	4974208	11/90	Nakamura, et al.	365	189.01	

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